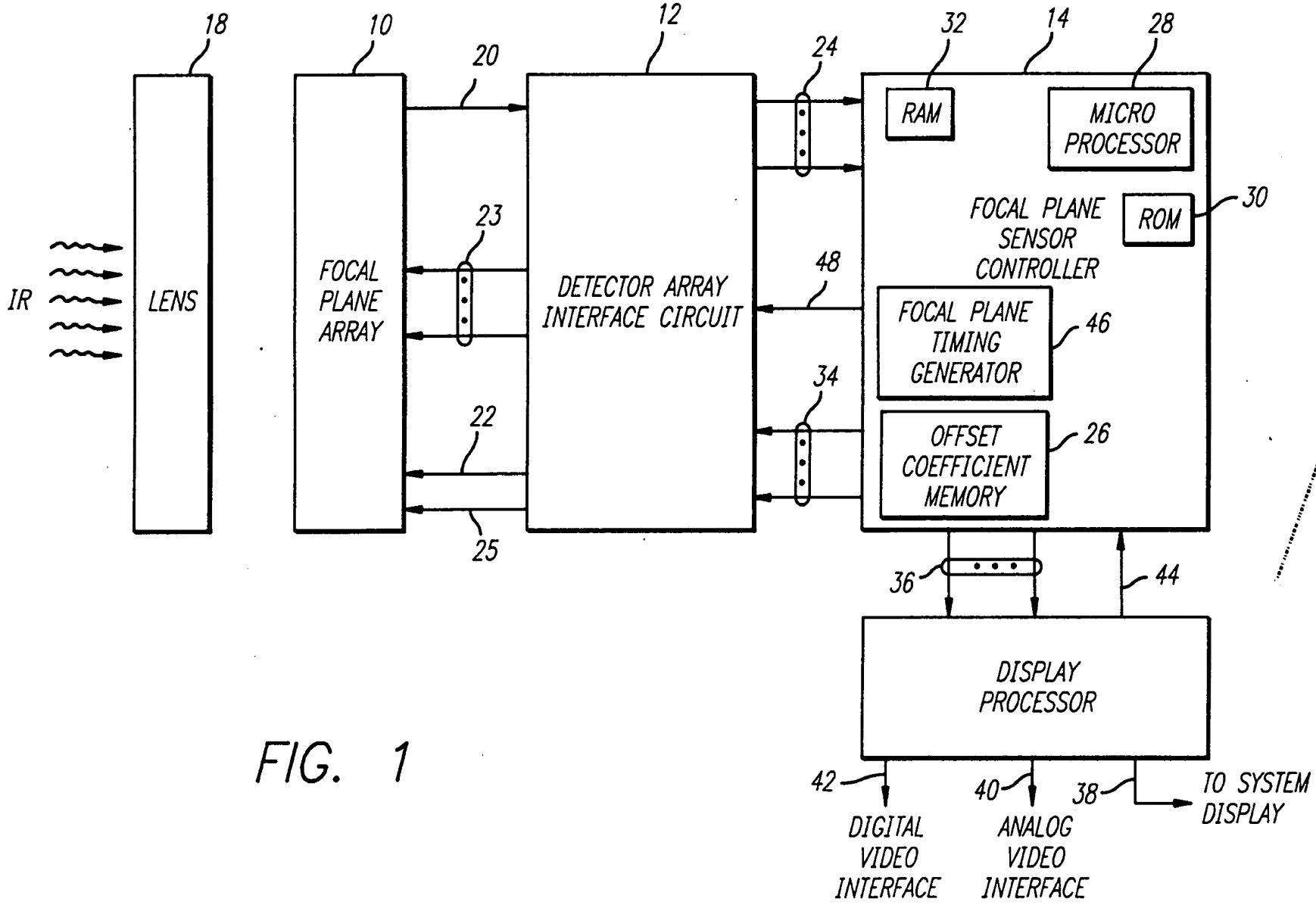
18
10
12
20
24
32
14
28
RAM
MICRO PROCESSOR
FOCAL PLANE SENSOR CONTROLLER
ROM
30
IR
LENS
FOCAL PLANE ARRAY
23
DETECTOR ARRAY INTERFACE CIRCUIT
48
FOCAL PLANE TIMING GENERATOR
46
34
22
OFFSET COEFFICIENT MEMORY
26
25
36
44
DISPLAY PROCESSOR
42
40
38
TO SYSTEM DISPLAY
DIGITAL VIDEO INTERFACE
ANALOG VIDEO INTERFACE

FIG. 1

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O.G. FIG.
CLASS SUBCLASS

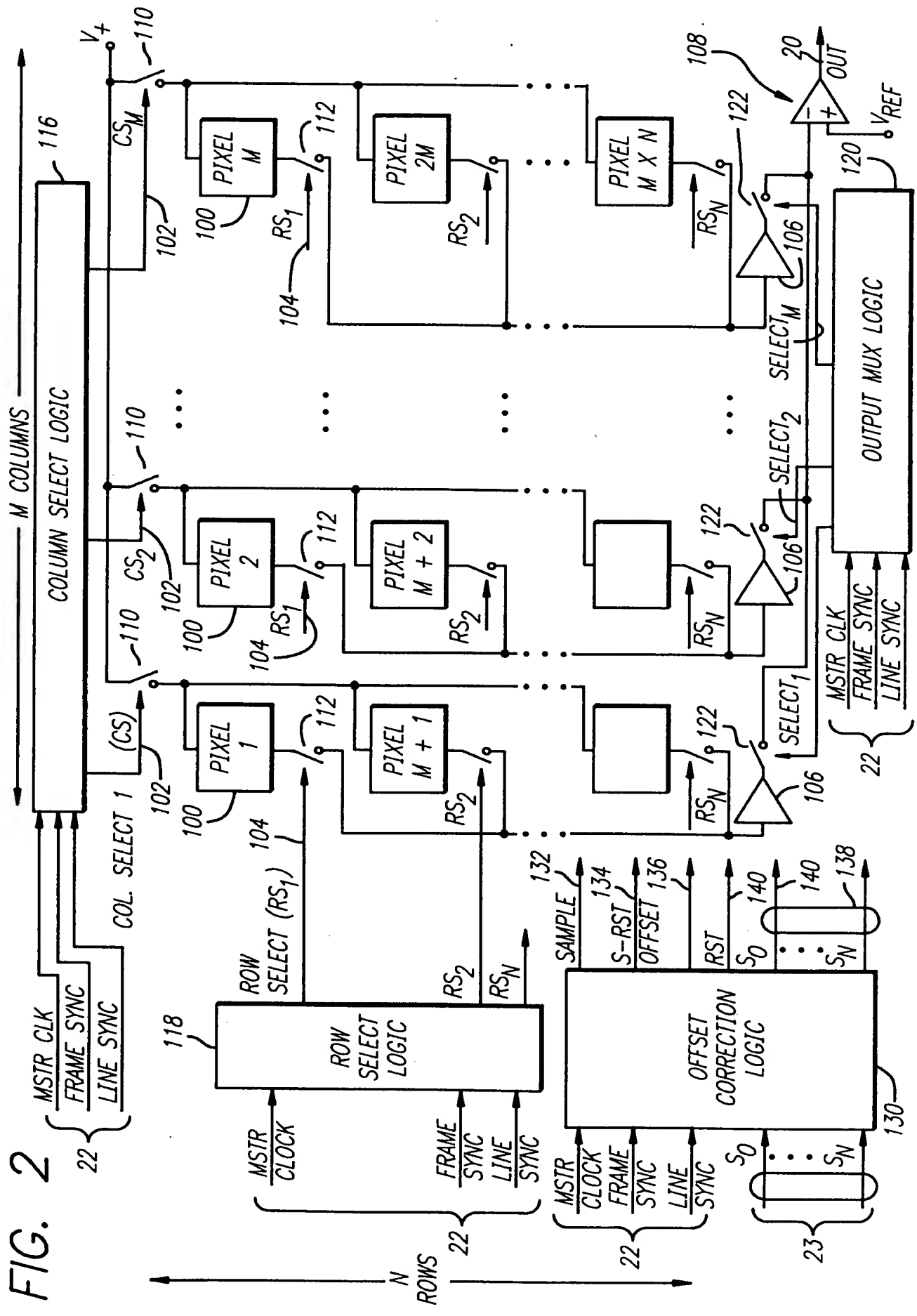


FIG. 2

APPROVED
BY
DRAFTSMAN
CLASS
SUBCLASS

FIG. 3A

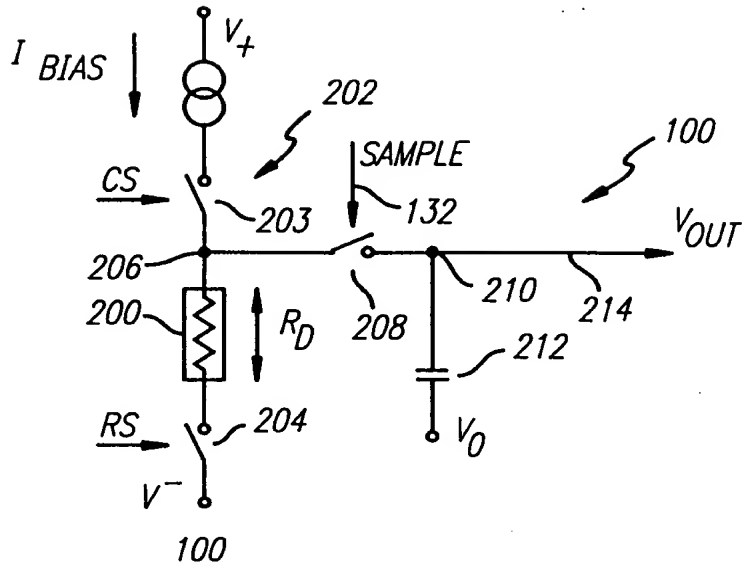
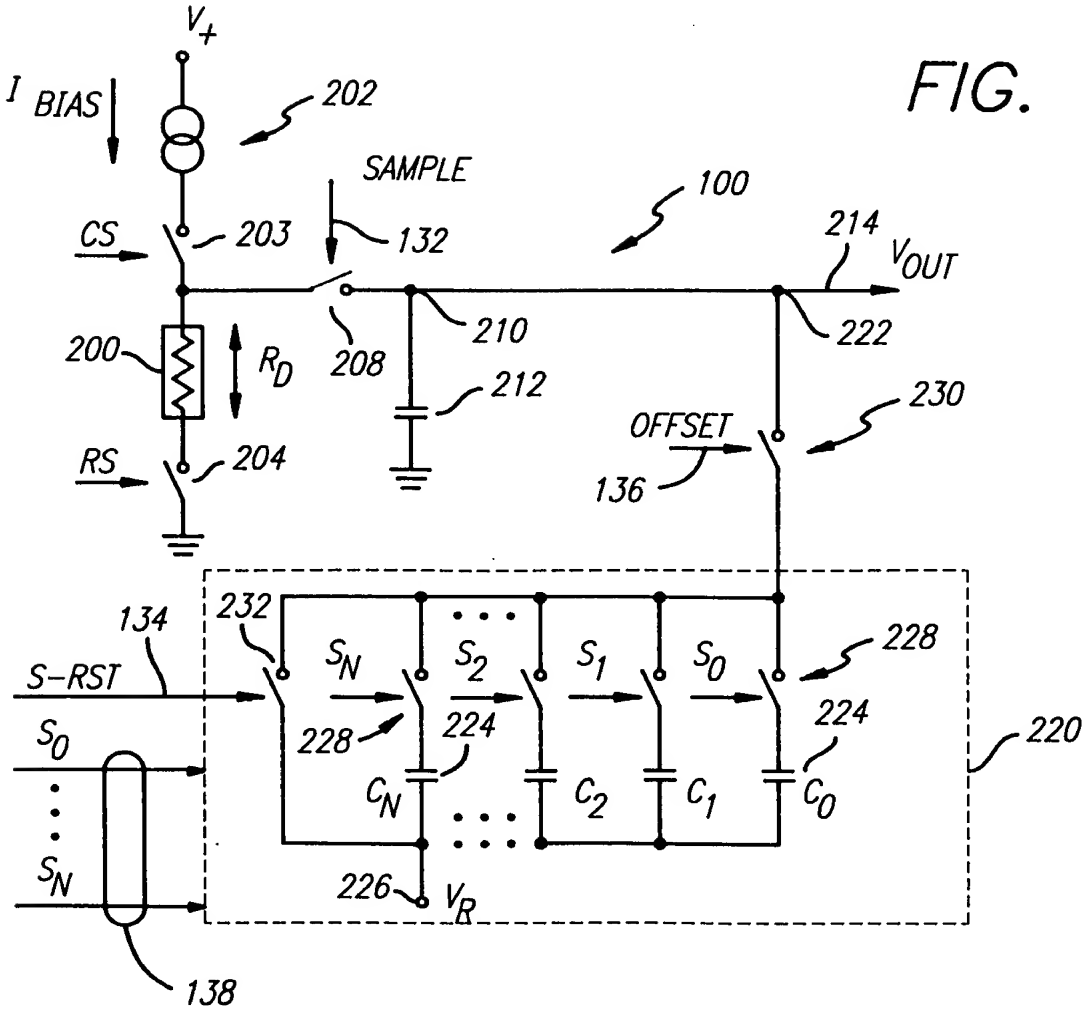
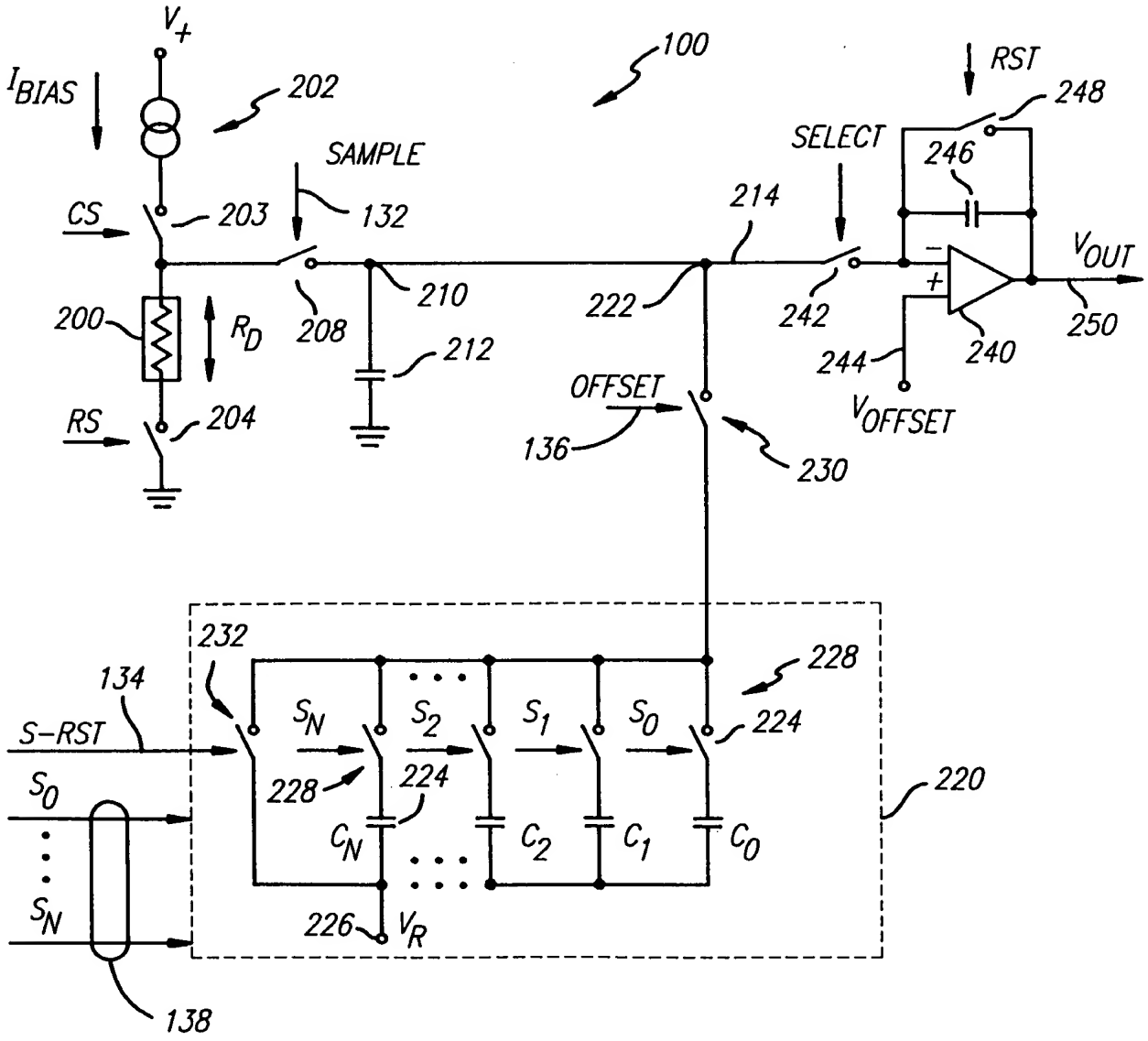


FIG. 3B



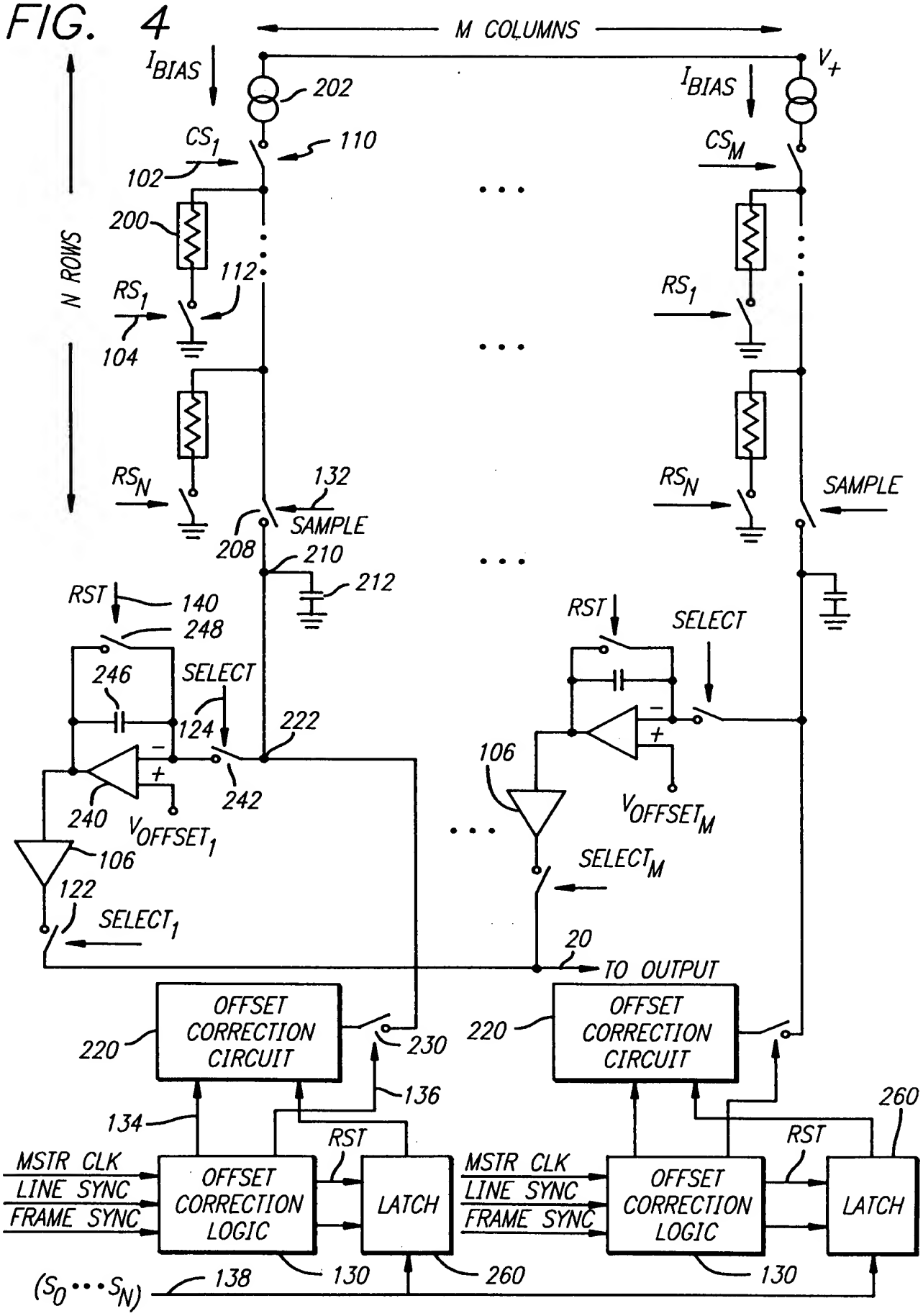
APPROVED BY DRAFTSMAN
O.G. FIG.
CLASS SUBCLASS

FIG. 3C



O.G. FIG.
CLASS SUBCLASS
DRAFTSMAN

FIG. 4



APPROVED BY DRAFTSMAN | O.G. FIG. CLASS SUBCLASS

FIG. 5A

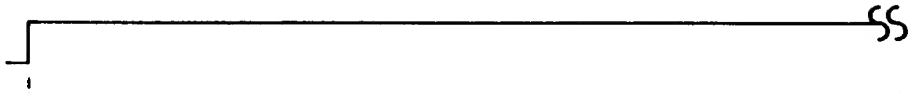


FIG. 5B

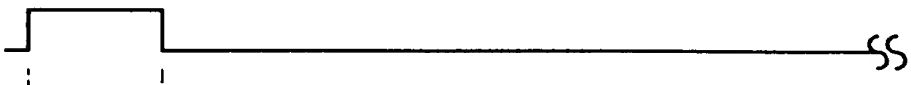


FIG. 5C

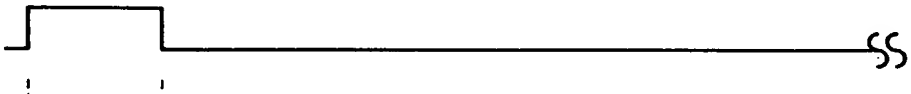


FIG. 5D

FIG. 5E

FIG. 5F

FIG. 5G

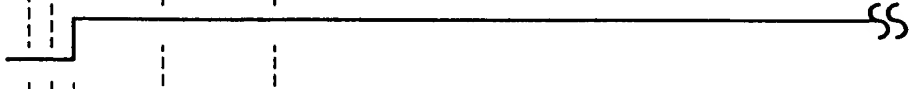


FIG. 5H

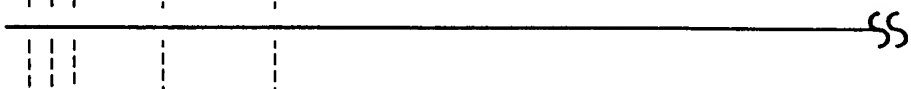


FIG. 5I

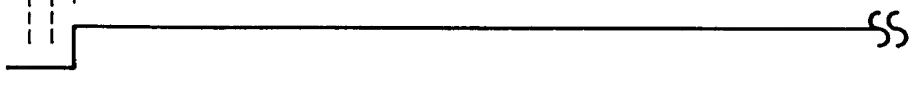


FIG. 5J

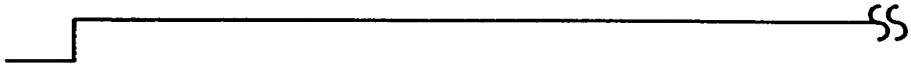
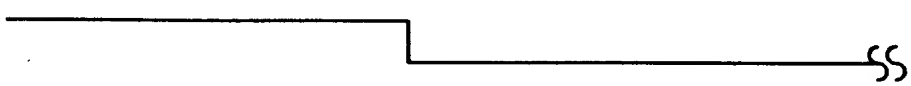


FIG. 5K



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O.G. FIG.
CLASS
SUBCLASS

FIG. 6A

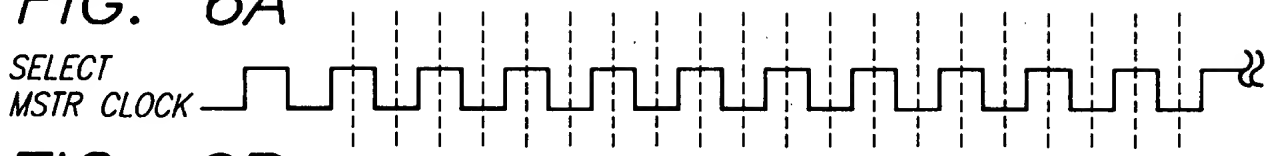


FIG. 6B

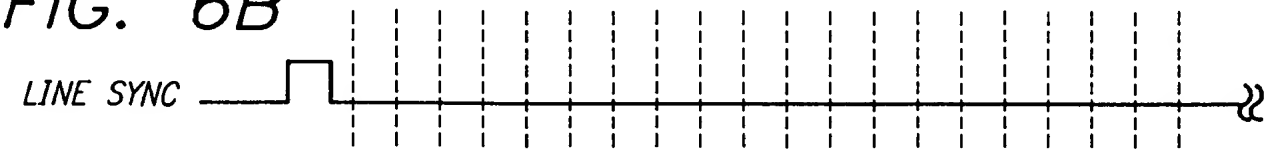


FIG. 6C

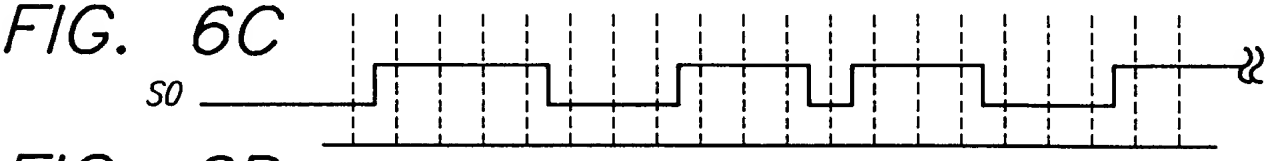


FIG. 6D

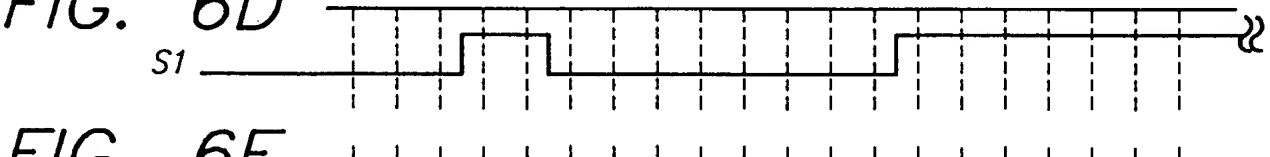


FIG. 6E

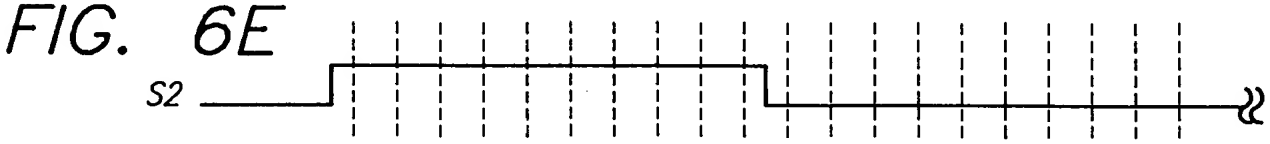


FIG. 6F

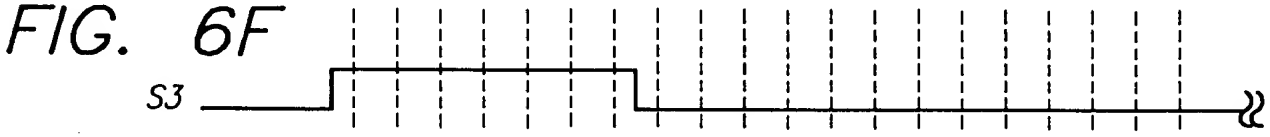


FIG. 6G

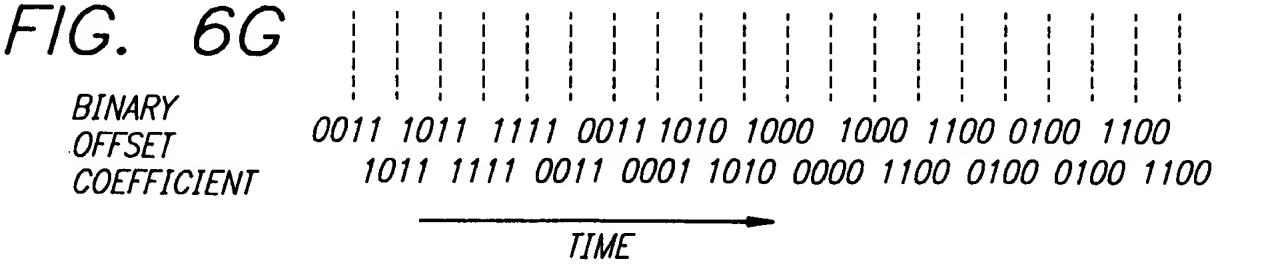
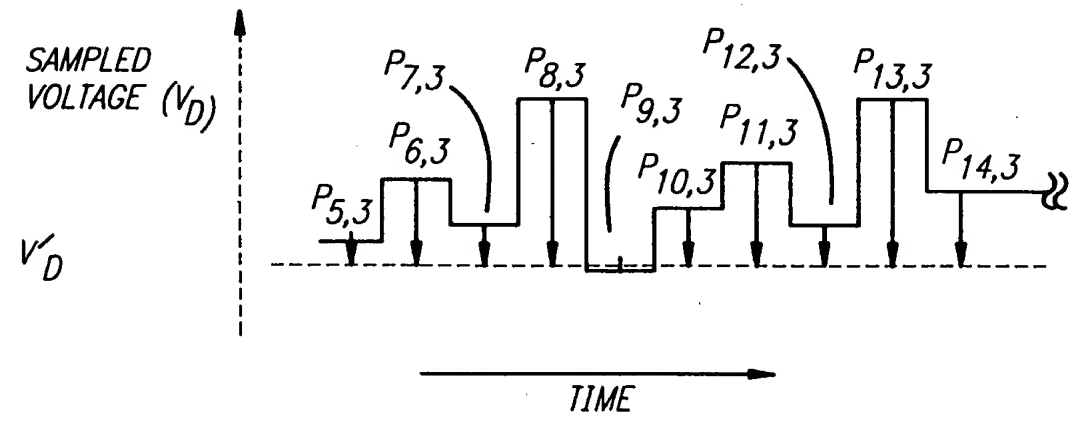


FIG. 7



O.G. FIG.
CLASS
SUBCLASS
DRAFTSMAN

FIG. 8

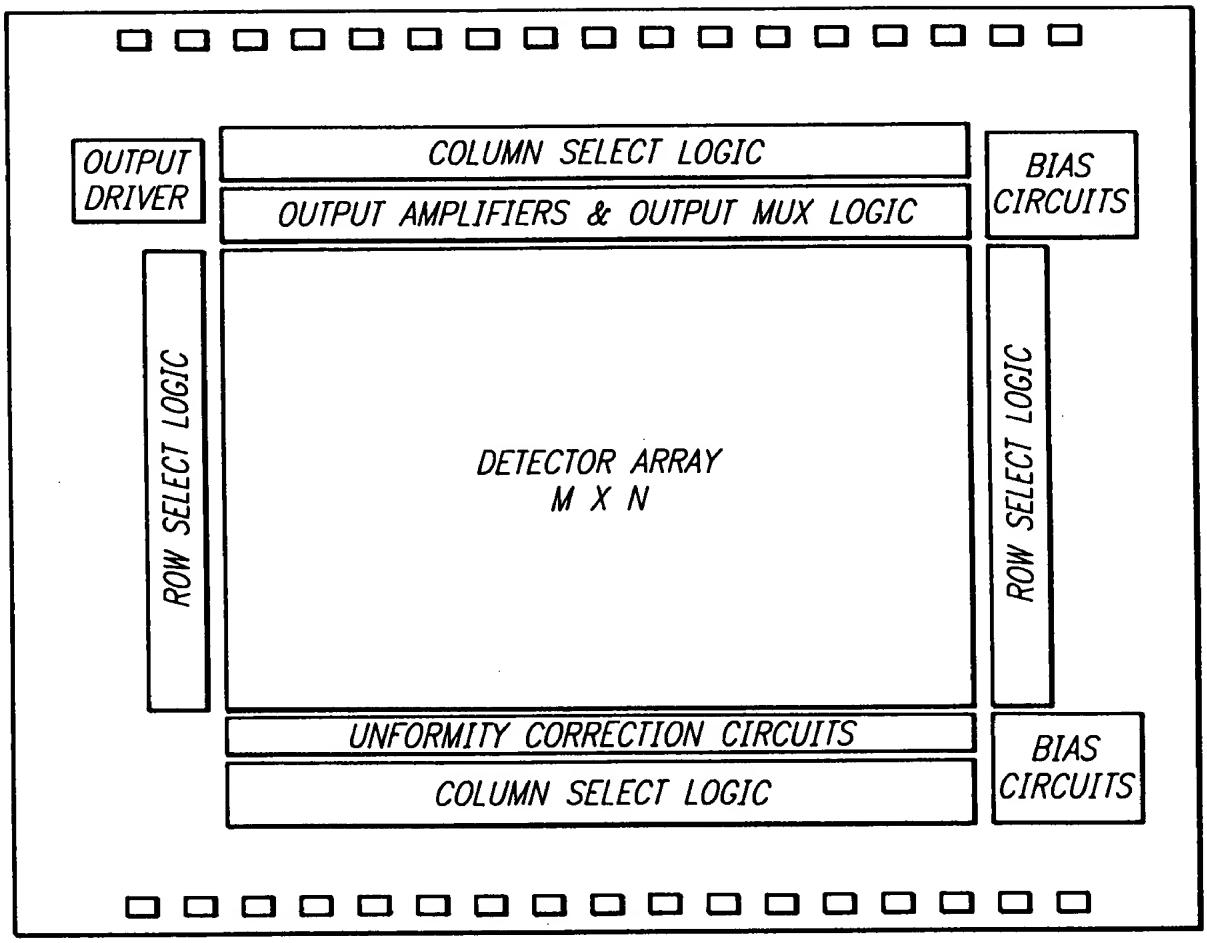
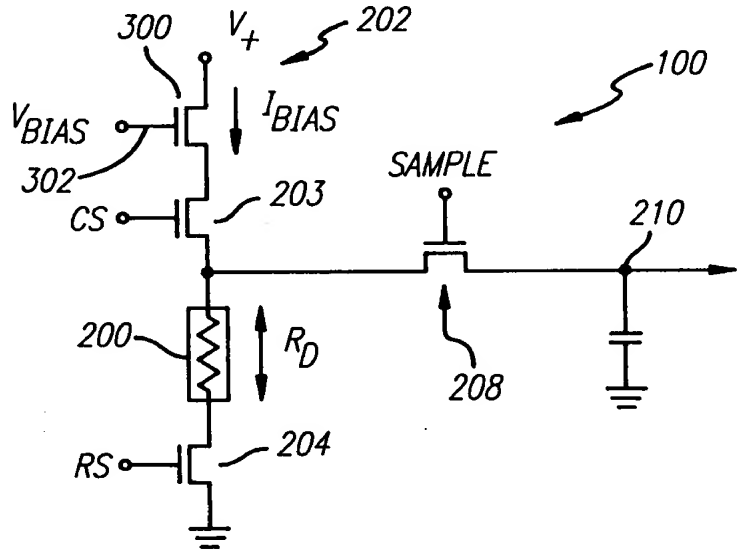


FIG. 9



APPROVED BY DRAFTSMAN | O.G. FIG | CLASS | SUBCLASS

FIG. 10

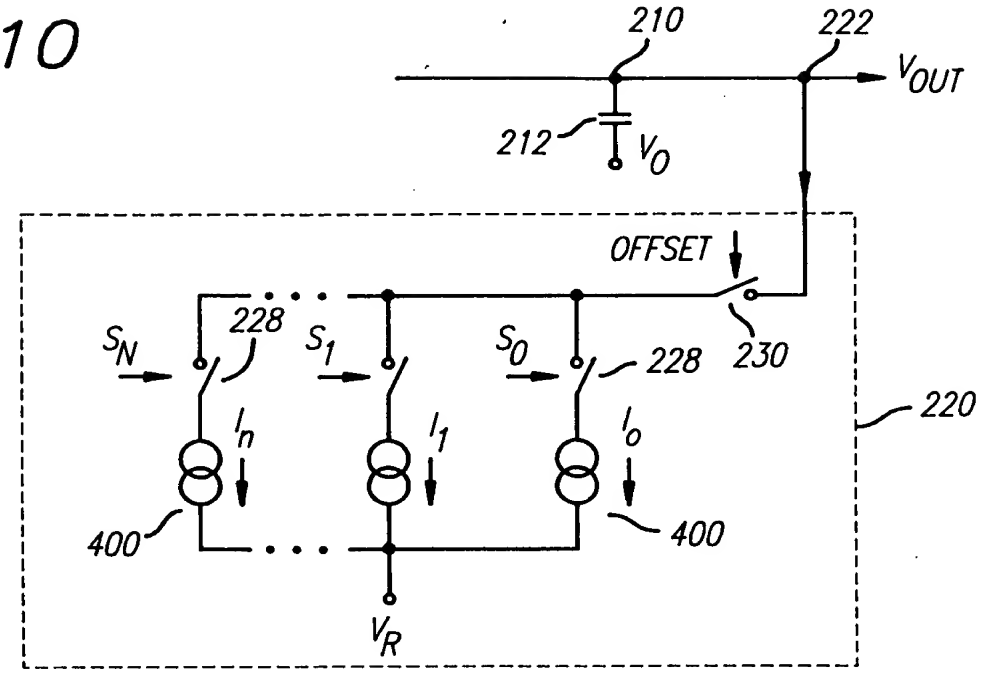
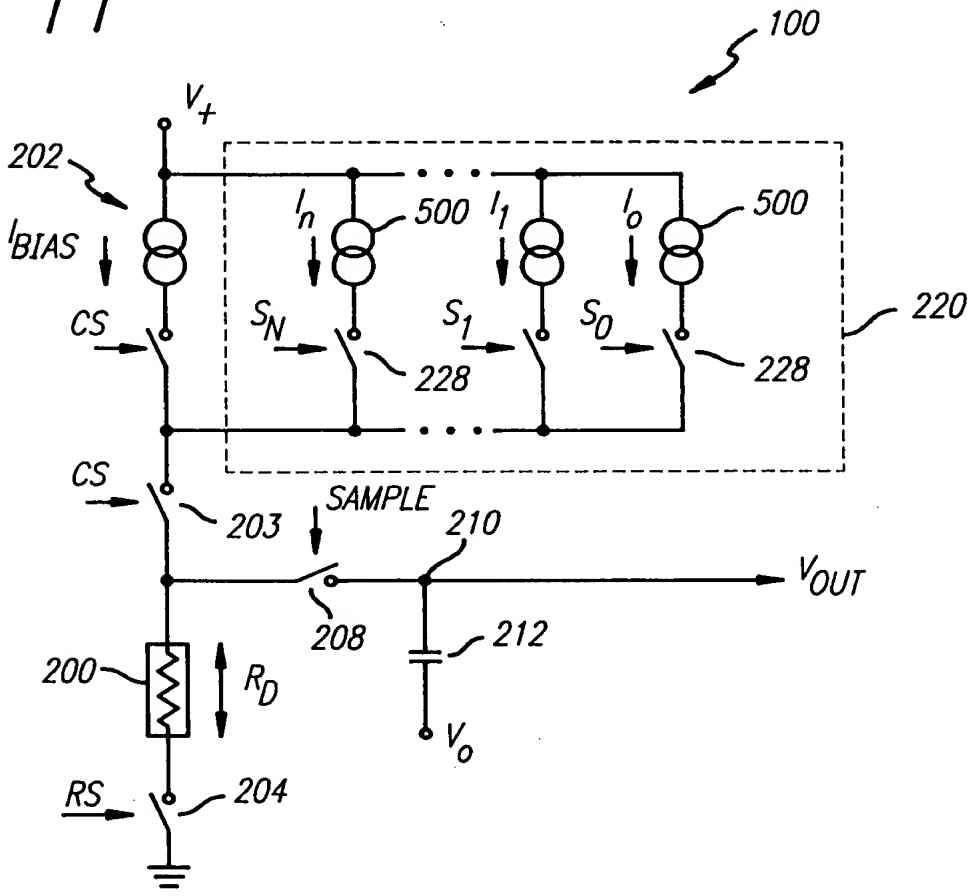


FIG. 11



APPROVED O.G. FIG.
BY CLASS SUBCLASS
DRAFTSMAN

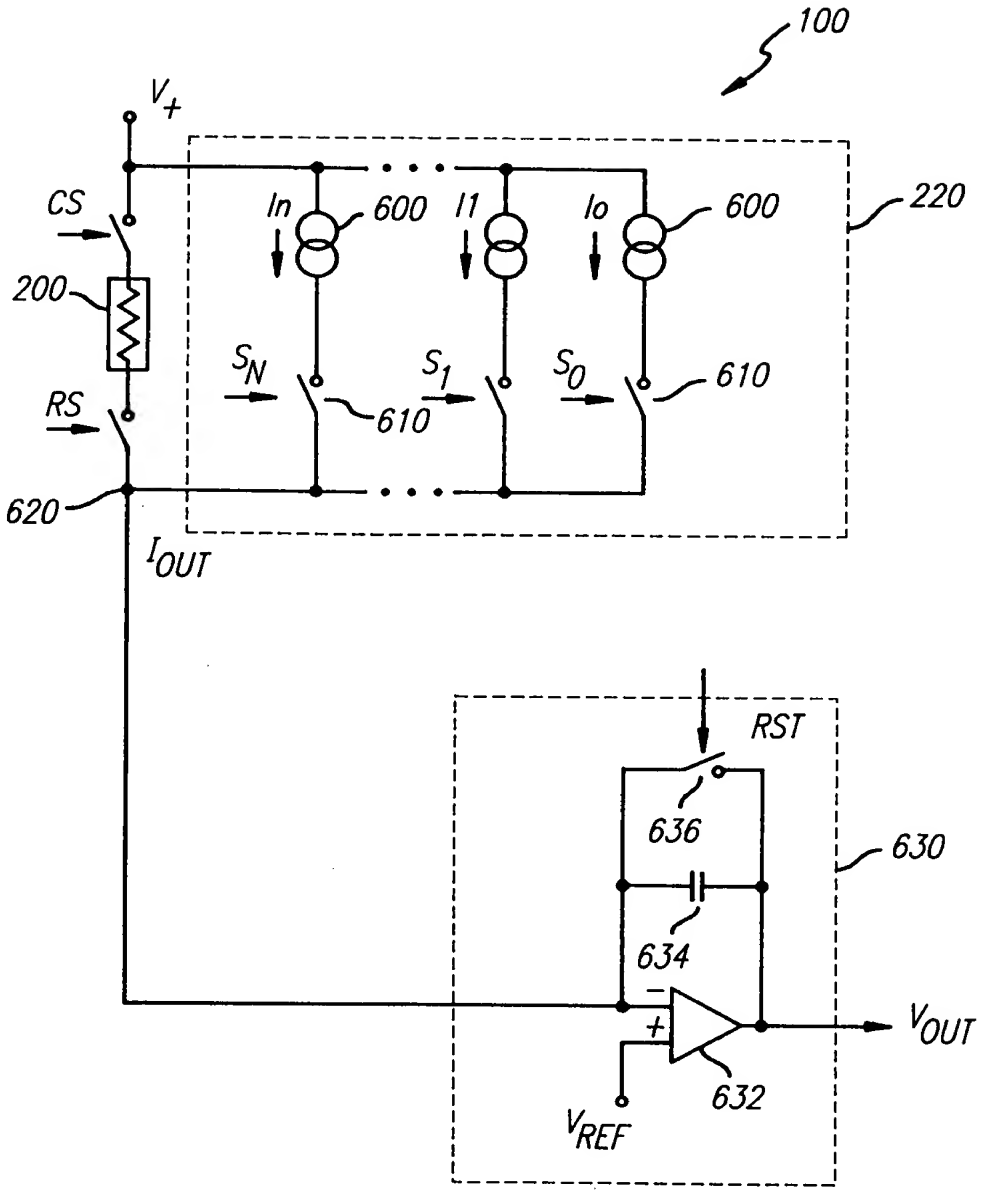



FIG. 12